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October 1, 2003

MAIL STOP PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Application of Hiroshi TANABE and Setsuo KANEKO
Title: SEMICONDUCTOR MANUFACTURING APPARATUS AND
MANUFACTURING METHOD OF THIN FILM SEMICONDUCTOR
DEVICE
Assignee: NEC CORPORATION
Our Ref: Q77312

Dear Sir:

This is a request for a Divisional Application of pending prior Application No. 08/866,331 (Confirmation No. 9640) filed May 30, 1997 of Hiroshi TANABE and Setsuo KANEKO entitled SEMICONDUCTOR MANUFACTURING APPARATUS AND MANUFACTURING METHOD OF THIN FILM SEMICONDUCTOR DEVICE.

This application is being filed under 37 C.F.R. § 1.53(b). Enclosed is a specification, including the claims and abstract, 10 sheets (Figures 1-10F) of drawings, and a copy of the Declaration as filed in the prior application. Also, an Information Disclosure Statement is being filed along with this Divisional Application.

The prior application is assigned to Group Art Unit 2822.

Priority is claimed from:

Country

JAPAN

Application No

137895/1996

Filing Date

May 31, 1996

The priority document was filed in parent Application No. 08/866,331.

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Divisional of Application No. 08/866,331
October 1, 2003
Page 2

The Government filing fee is calculated as follows:

Total claims	<u>4</u> - 20	=	<u> </u>	x	\$18.00	=	<u> </u>	\$0.00
Independent claims	<u>1</u> - 3	=	<u> </u>	x	\$84.00	=	<u> </u>	\$0.00
Base Fee								\$750.00

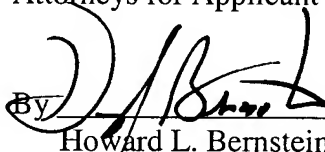
TOTAL FILING FEE								<u>\$750.00</u>
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A check for the statutory fee of \$750.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.

The application is timely filed.

Respectfully submitted,

SUGHRUE MION, PLLC
Attorneys for Applicant

By 
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